

FIG. 1

High Level Block Diagram of Edge Based Receiver for 2.5Gb/s operation.

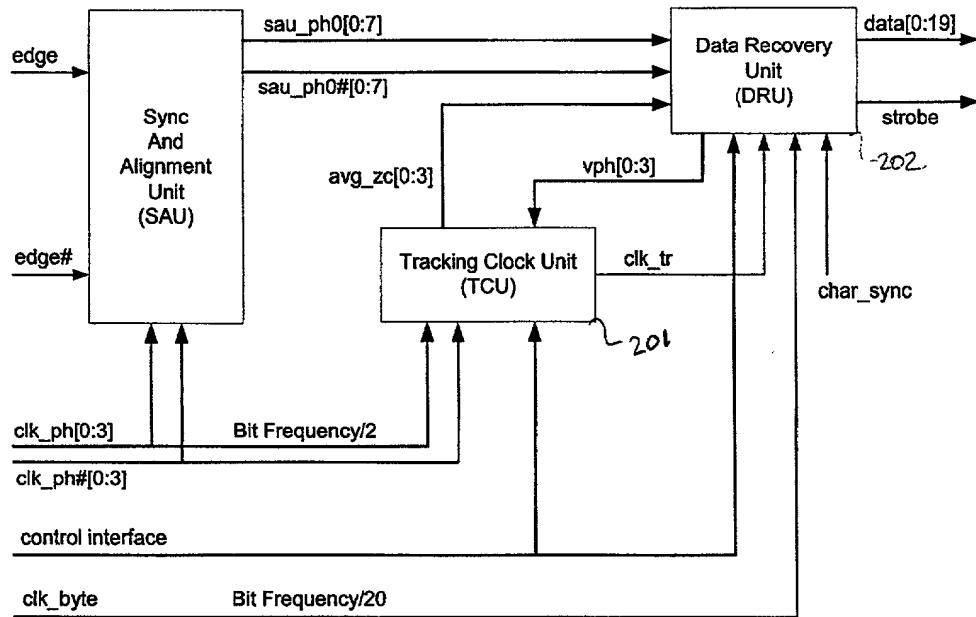


Fig. 2

*Edge Processing Block Diagram.*

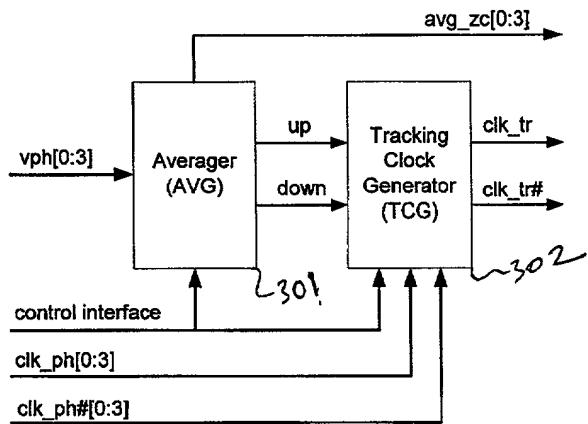
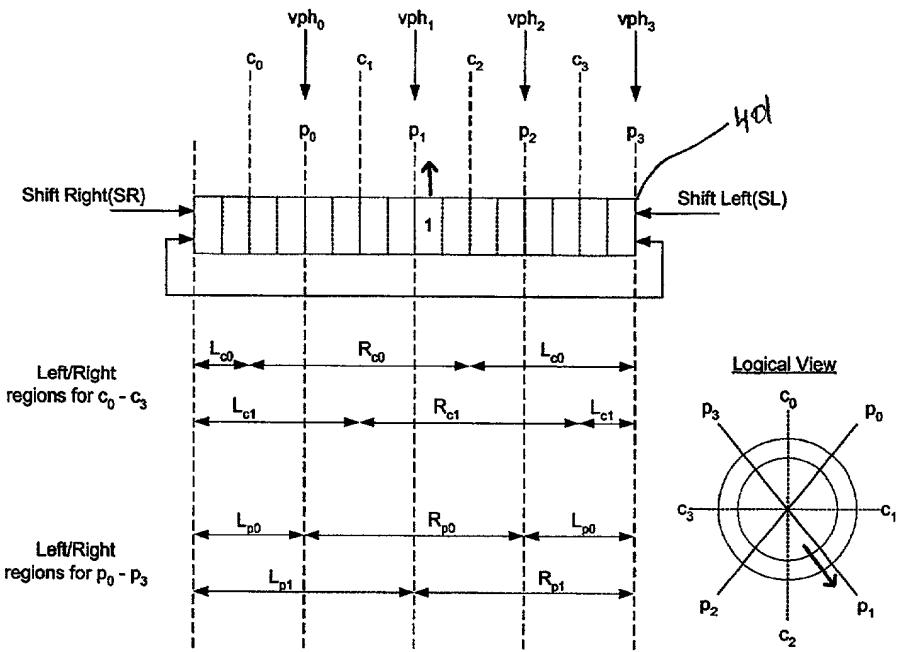


FIG.3

*Tracking Clock Unit (TCU) Block Diagram.*



Barrel Shifter for Phase Tracking.

FIG. 4

FIG. 5

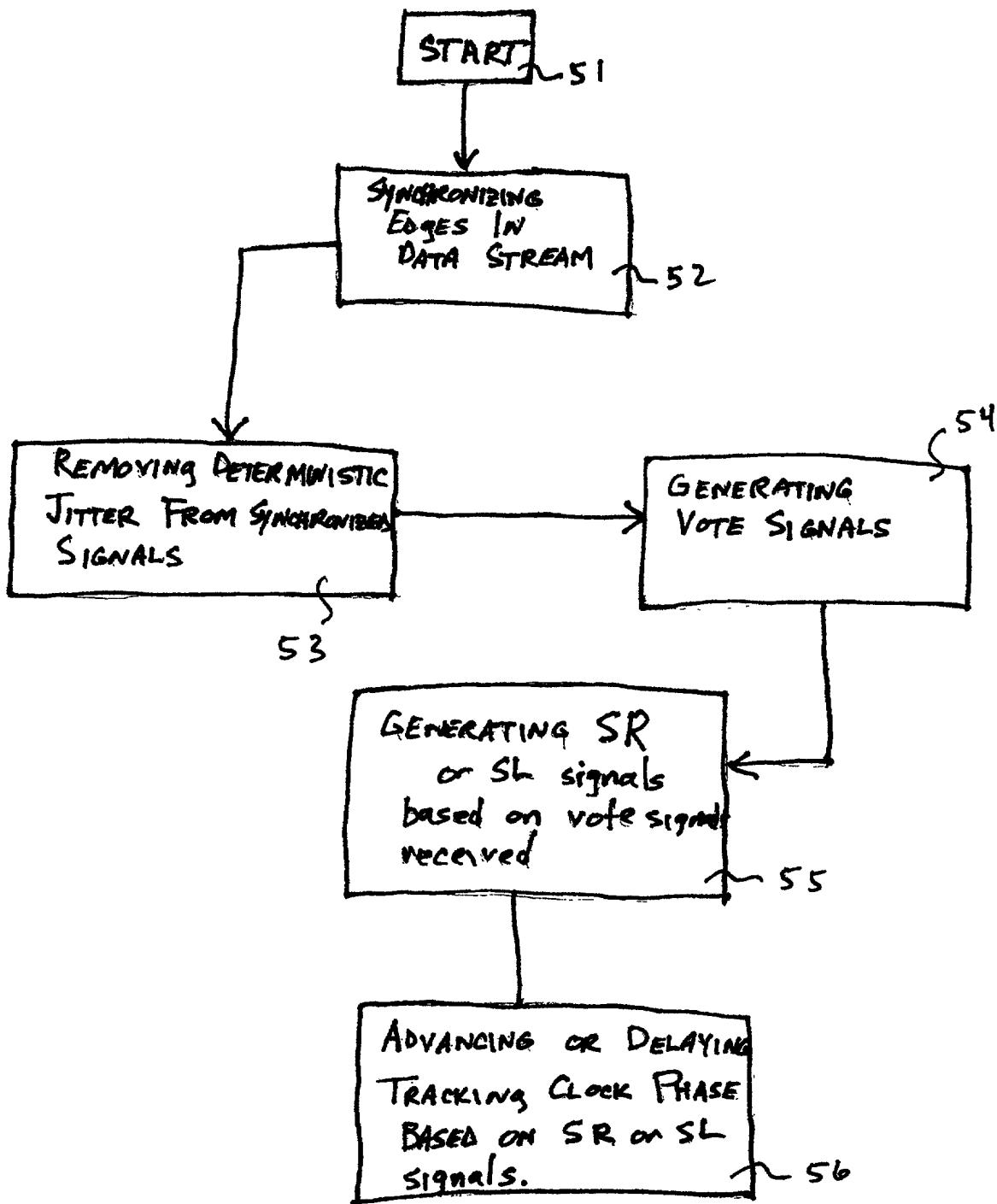


FIG 6 a

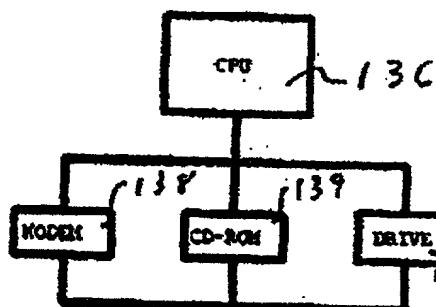


FIG 6 b



FIG 6 c

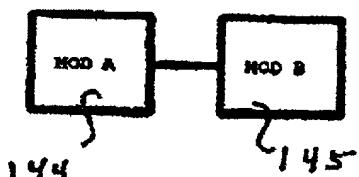


FIG 6 d

